

Figure 1b

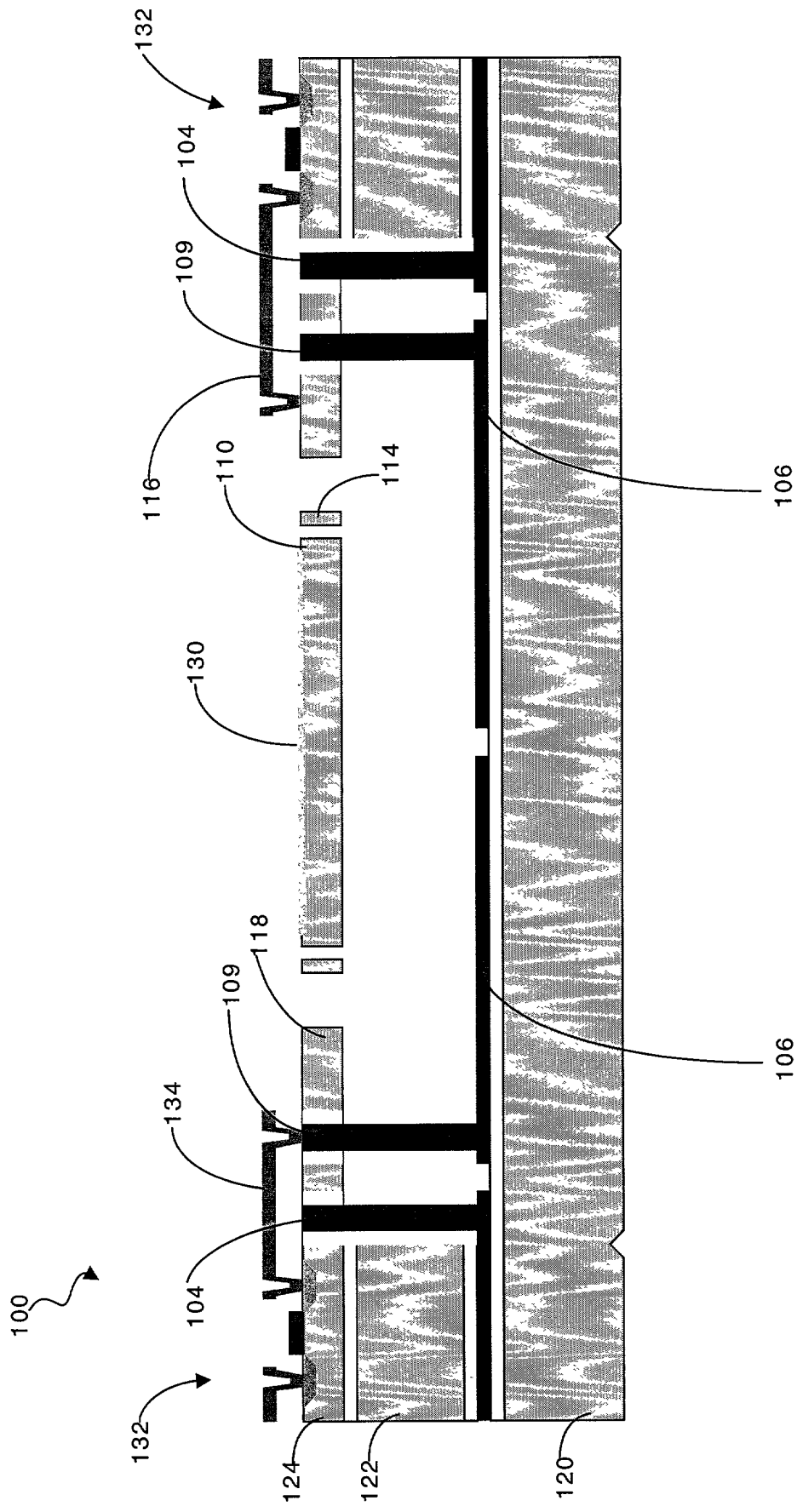


Figure 1c

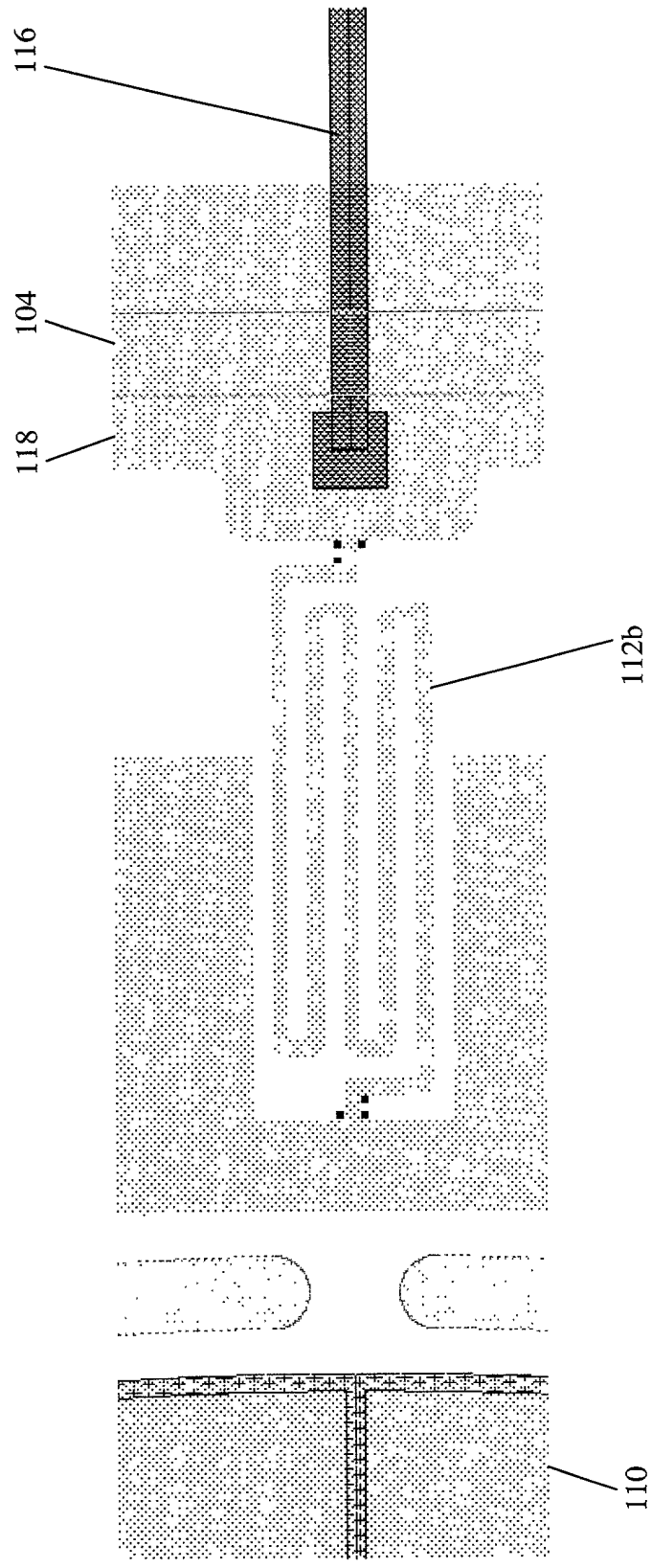


Figure 1d

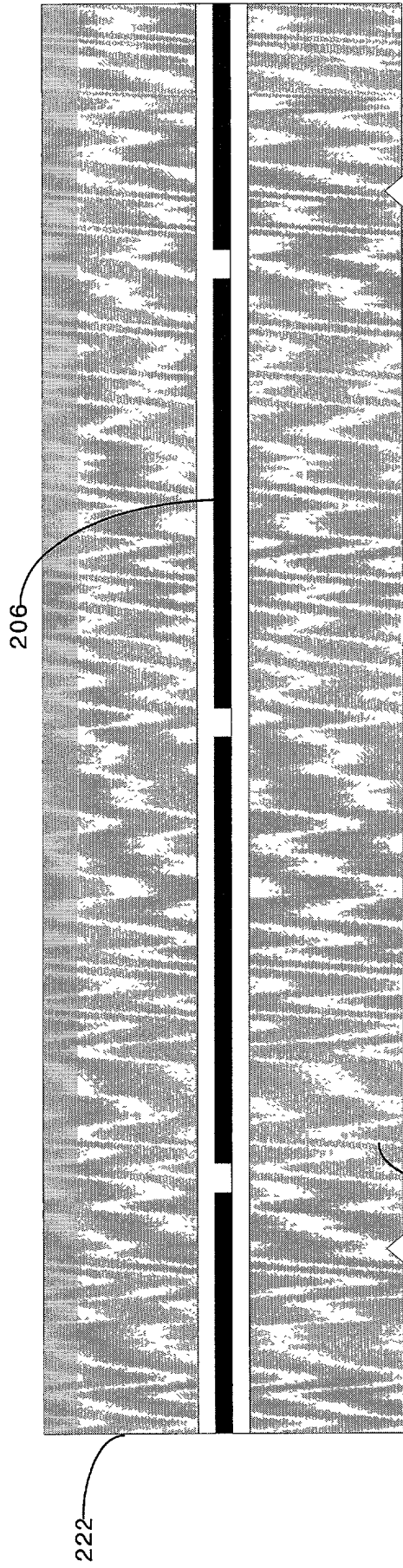


Figure 2a

200

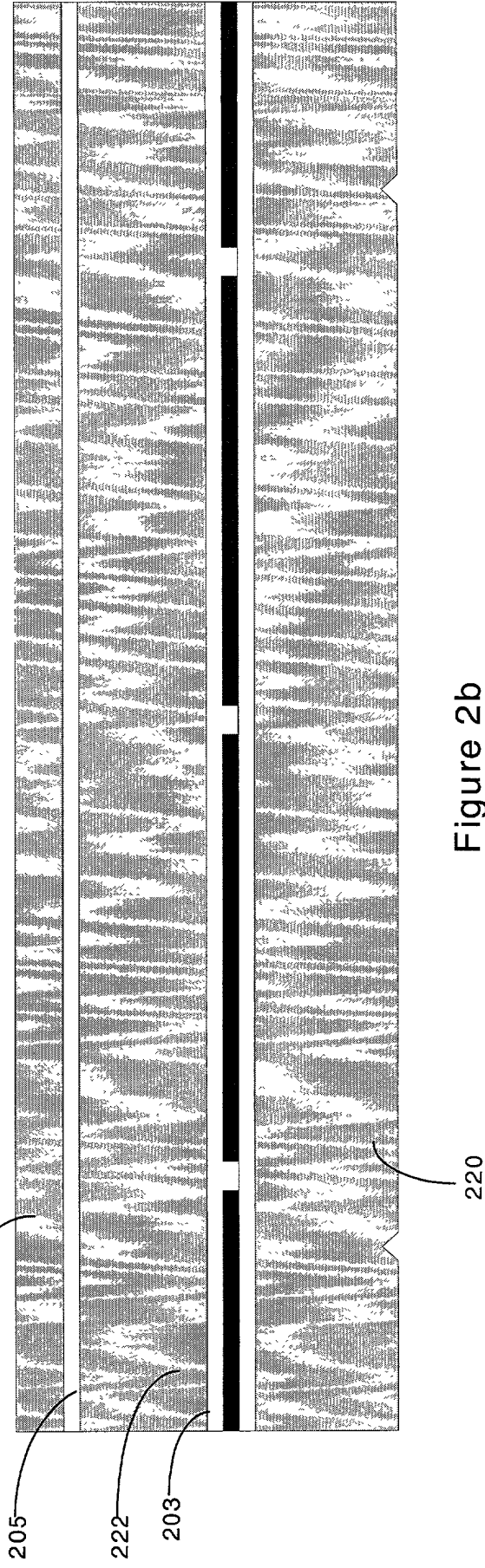


Figure 2b

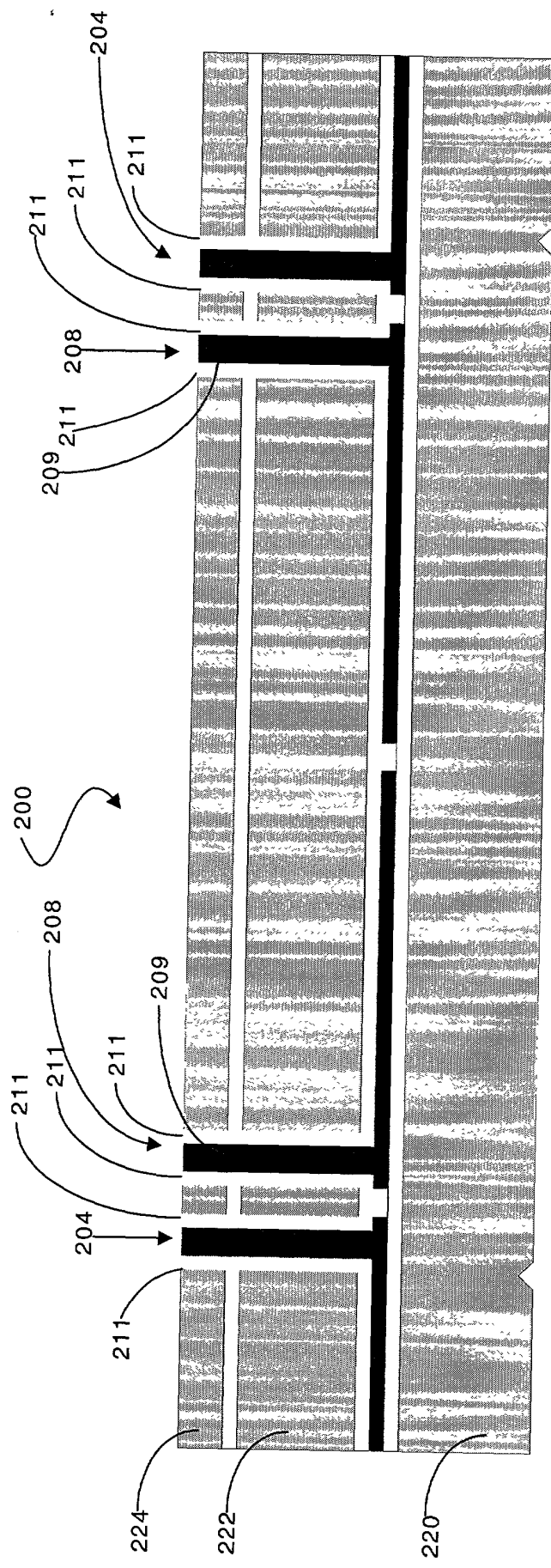


Figure 2c

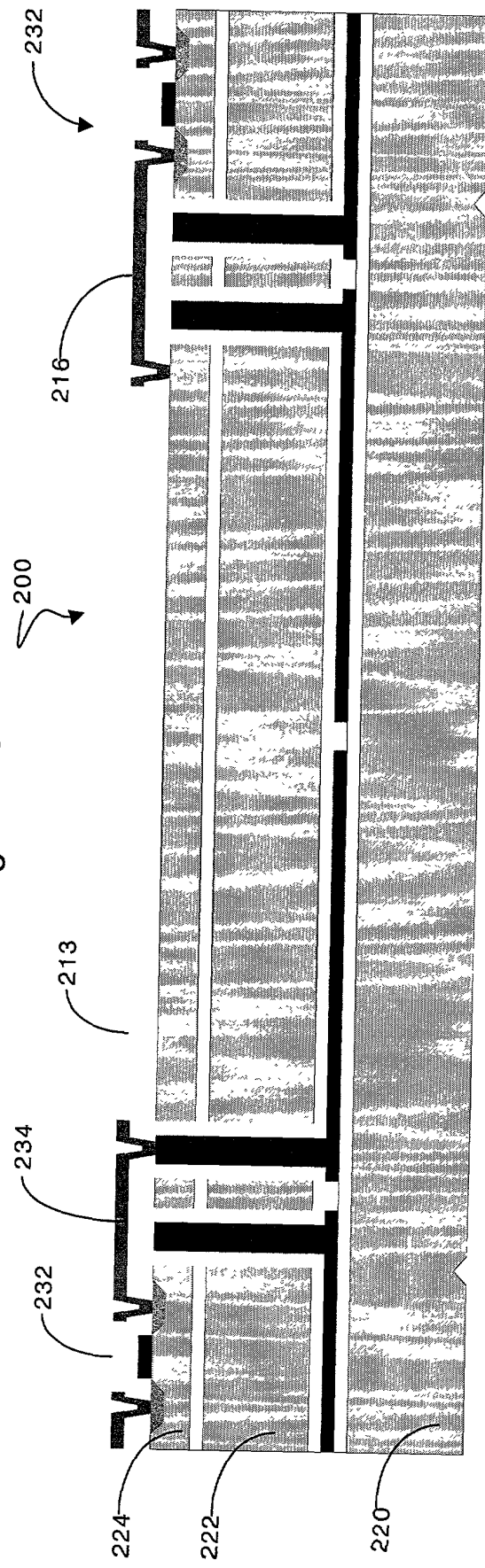


Figure 2d

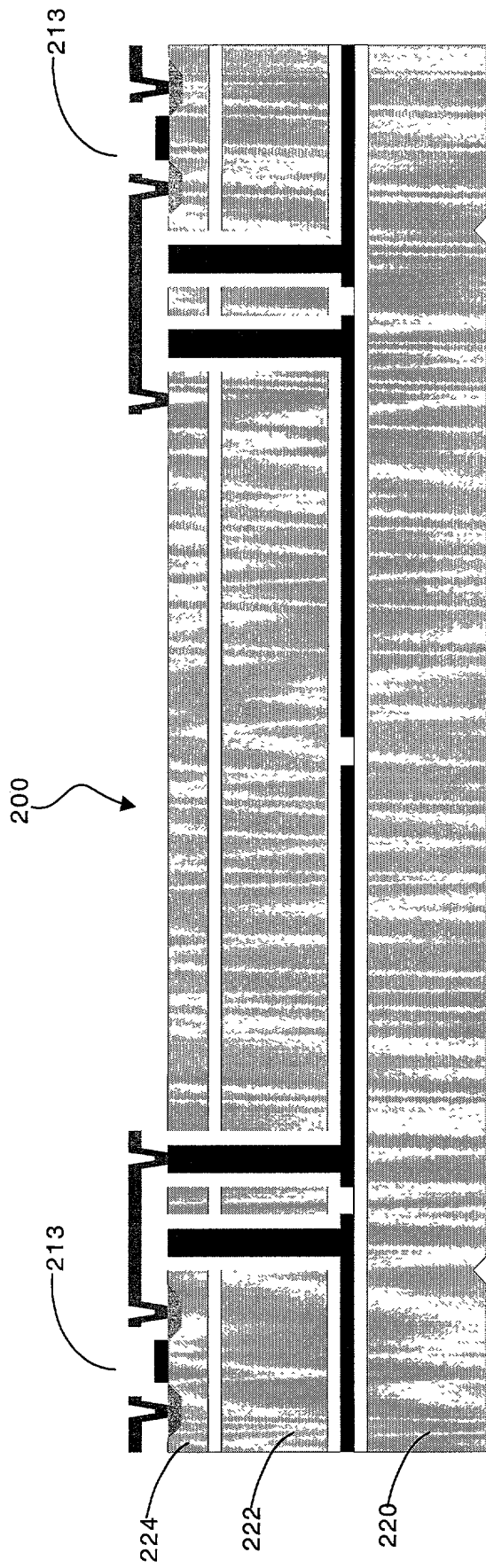


Figure 2e

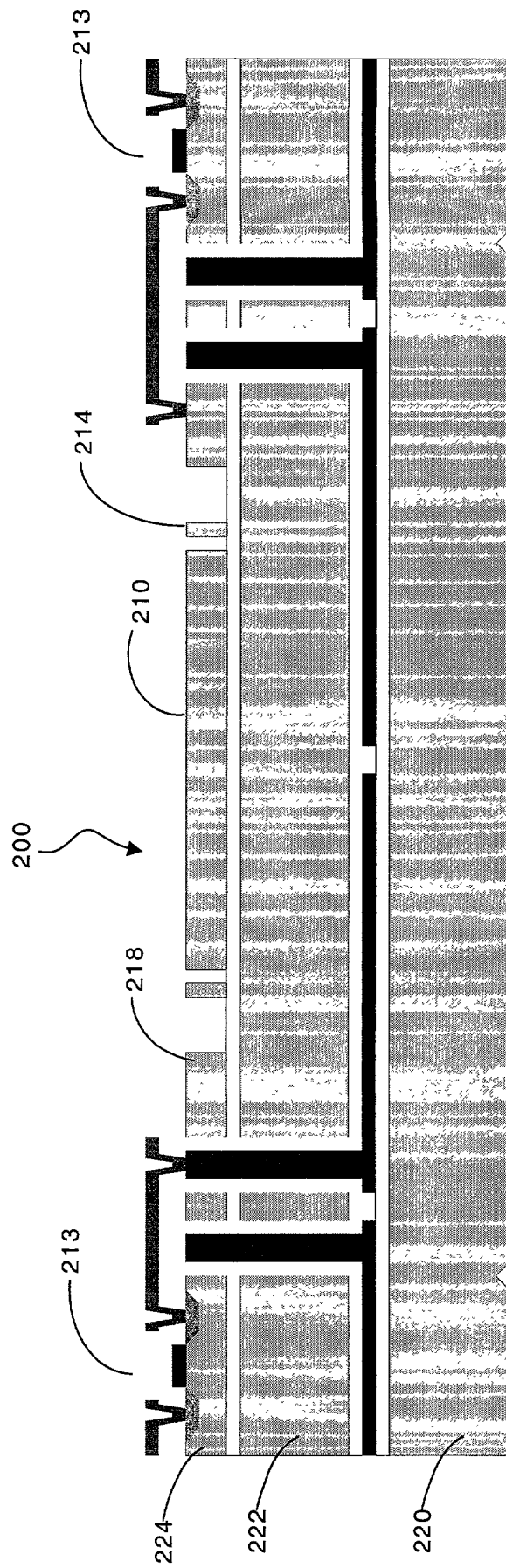


Figure 2f







FIG. 2i is a schematic diagram of a

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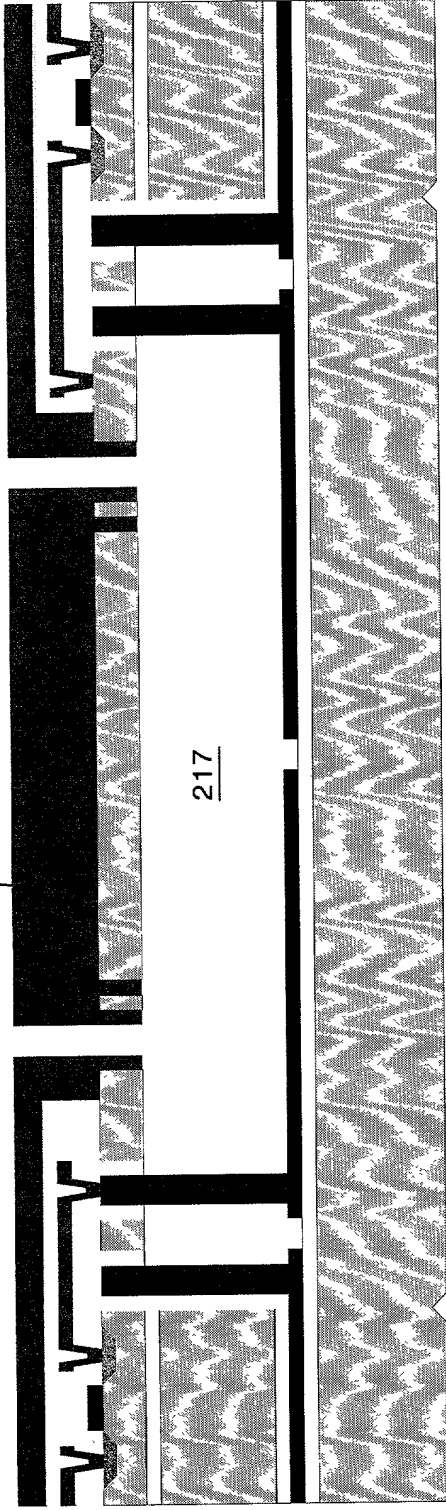


Figure 2i

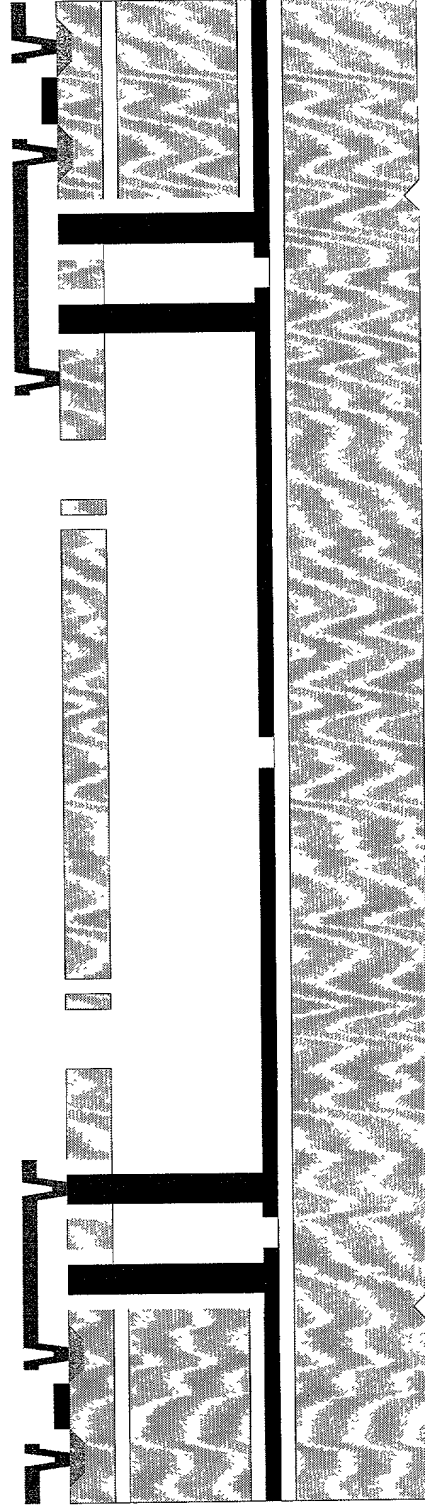


Figure 2j

300

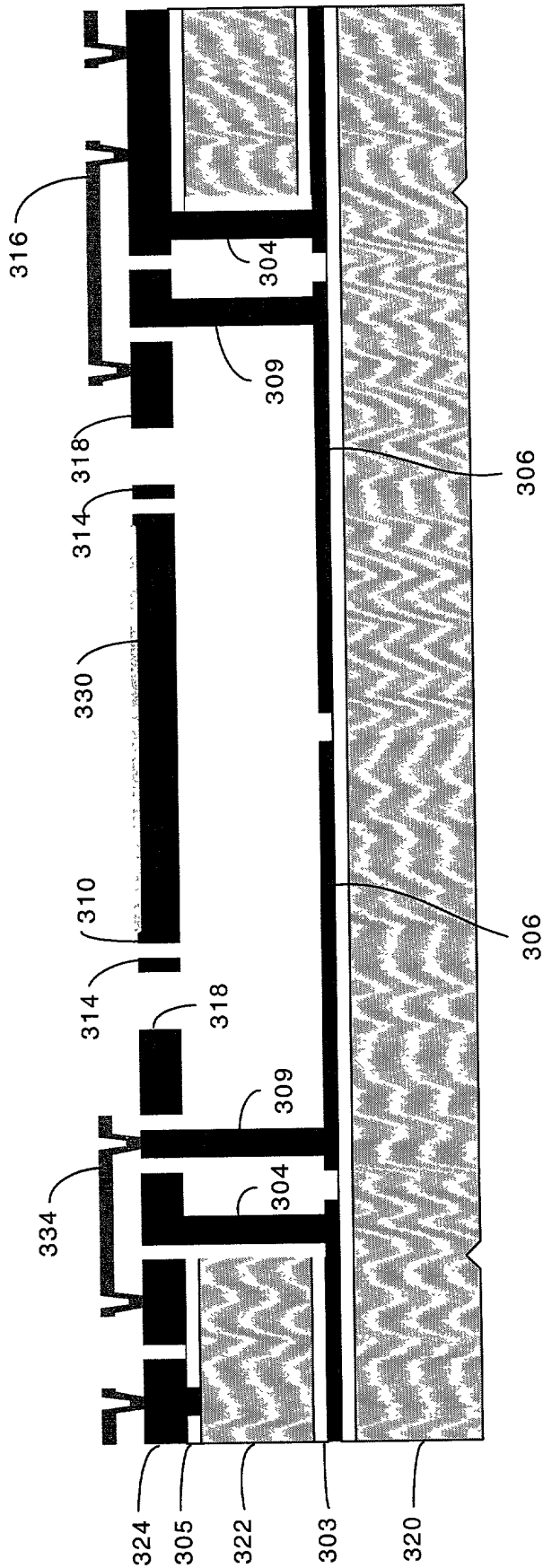


Figure 3

400

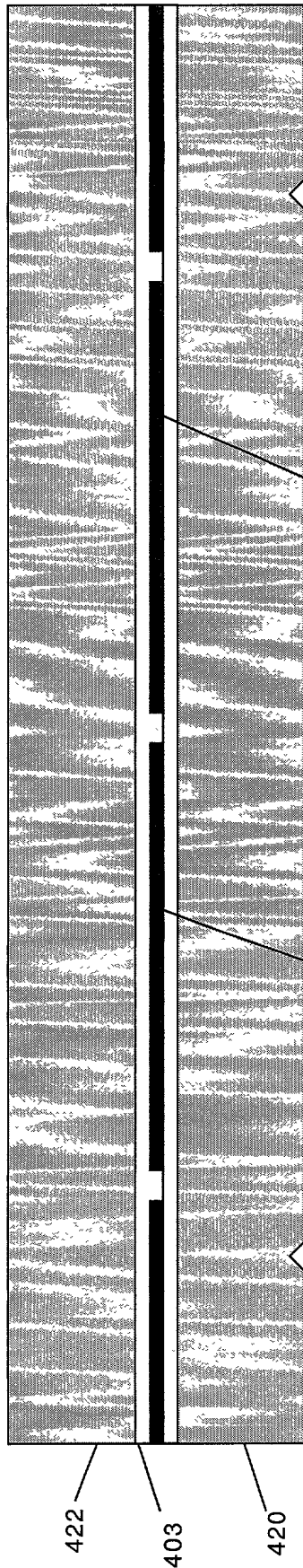


Figure 4a

400

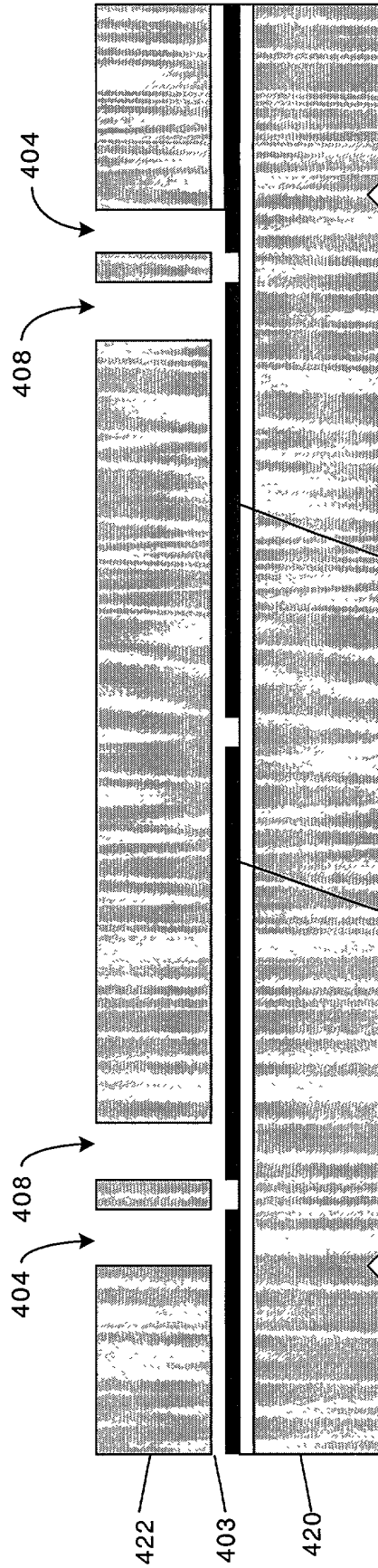


Figure 4b

400

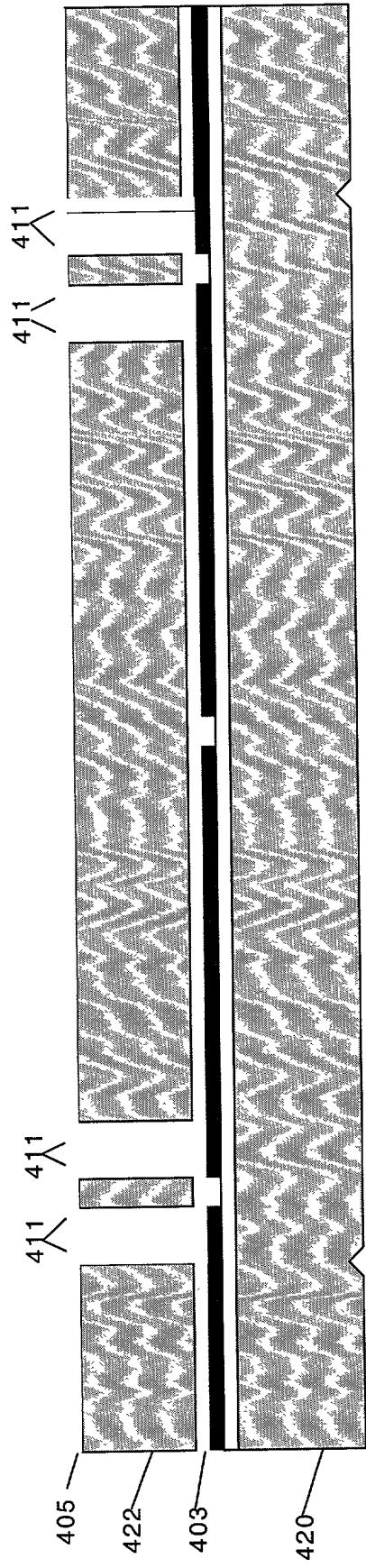


Figure 4c

400

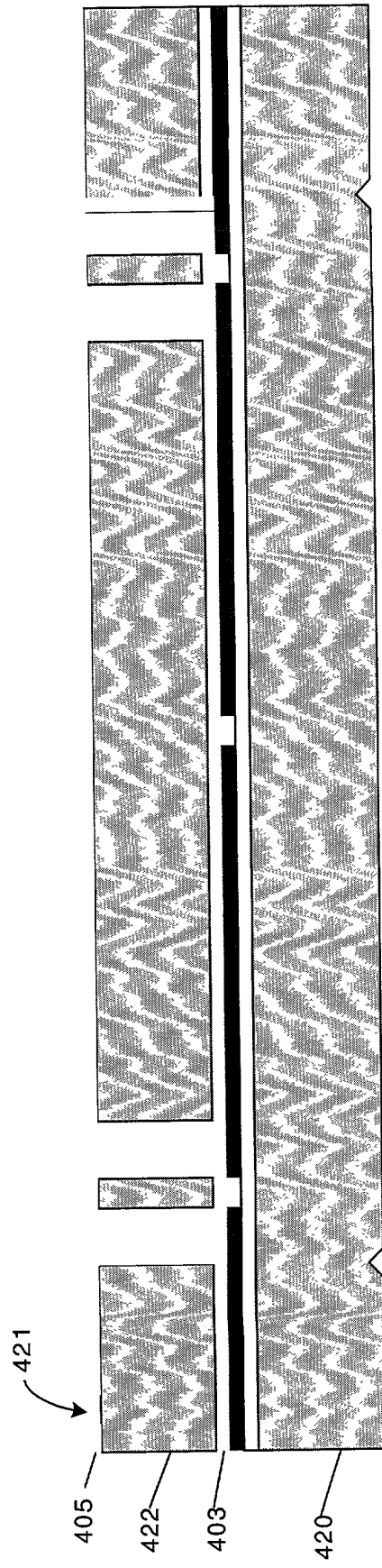


Figure 4d

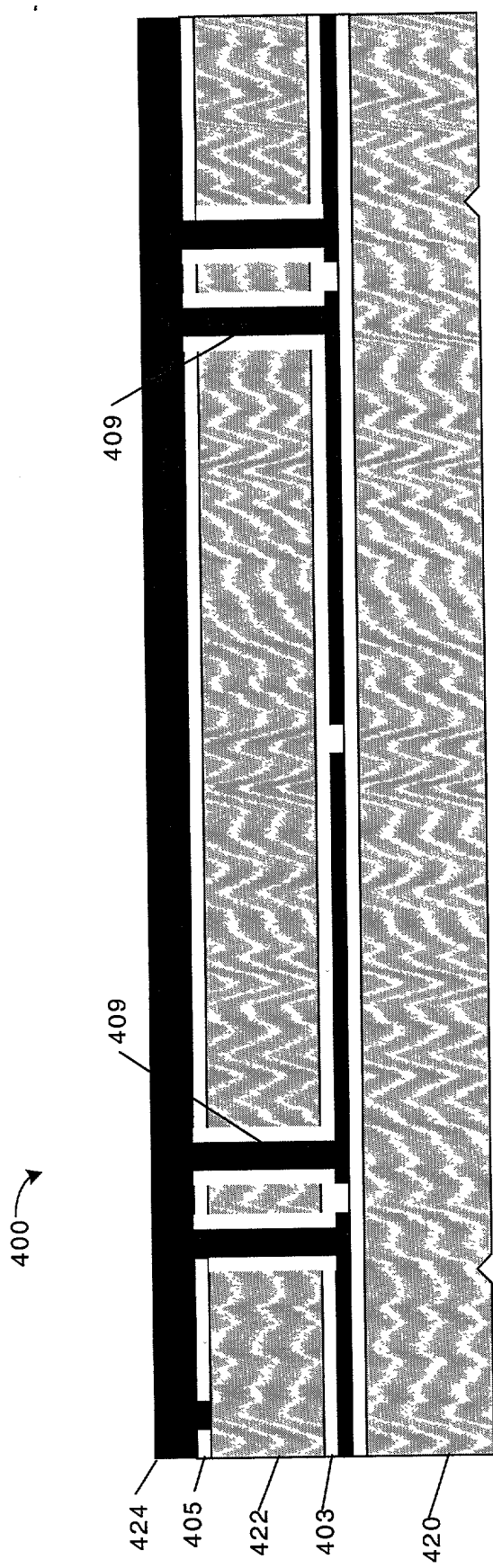


Figure 4e

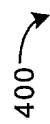


Figure 4f

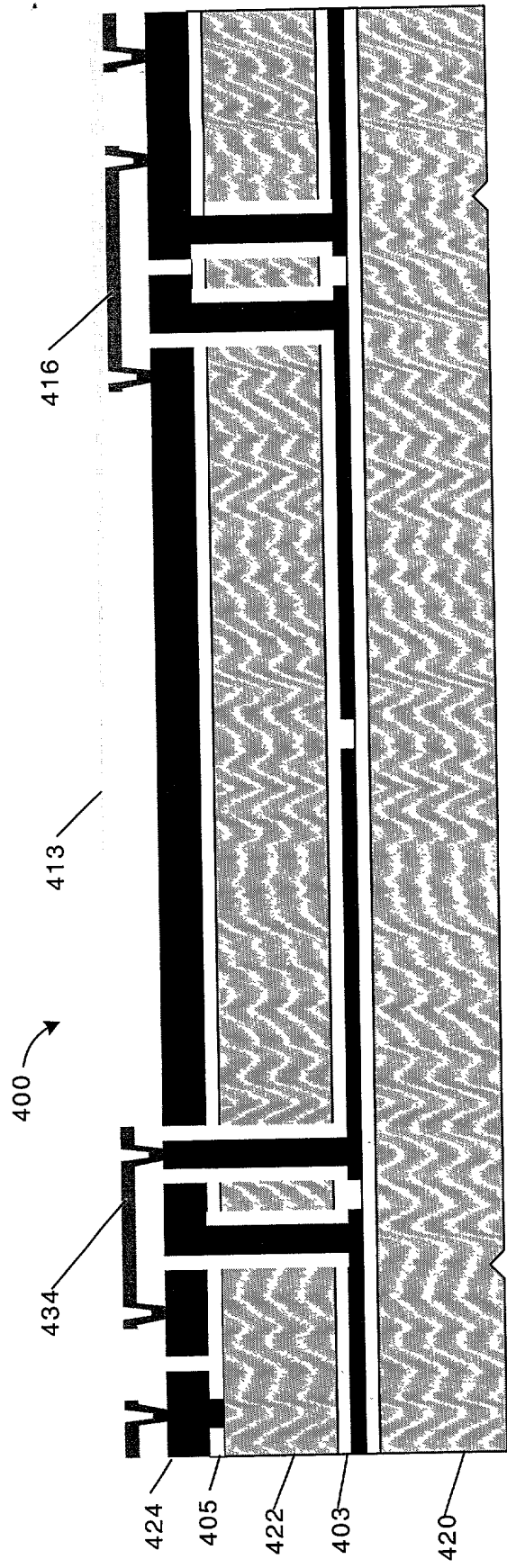


Figure 4g

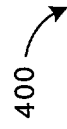


Figure 4h



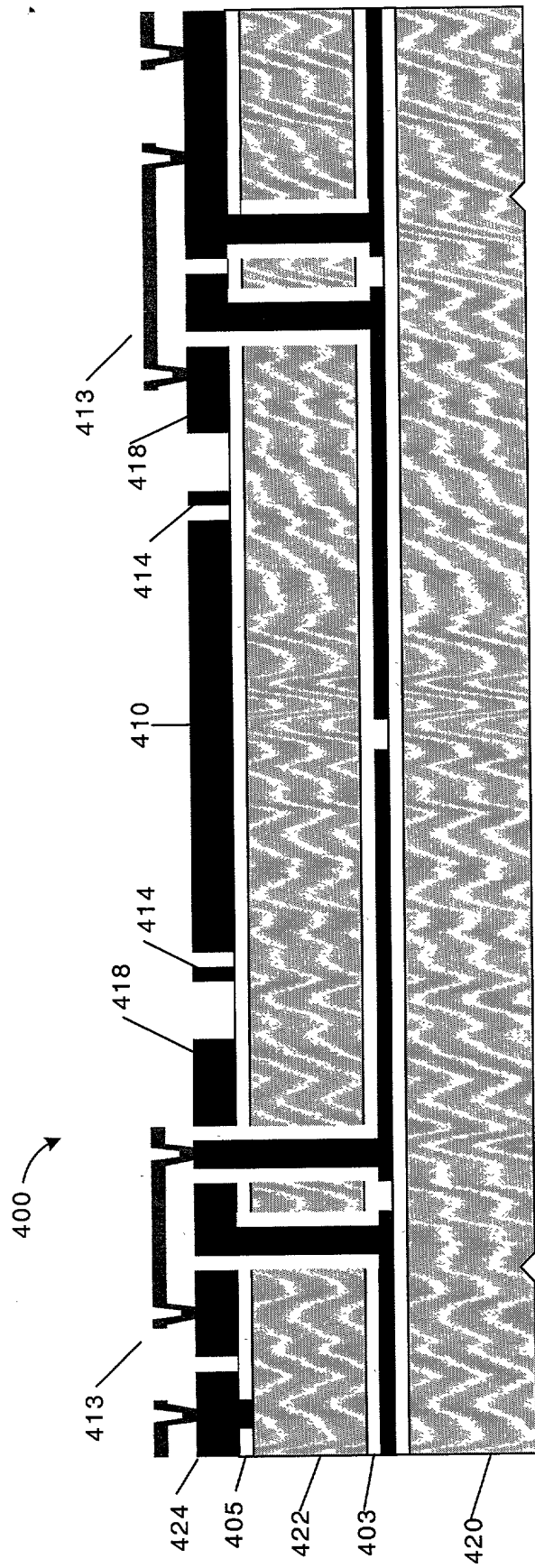


Figure 4i

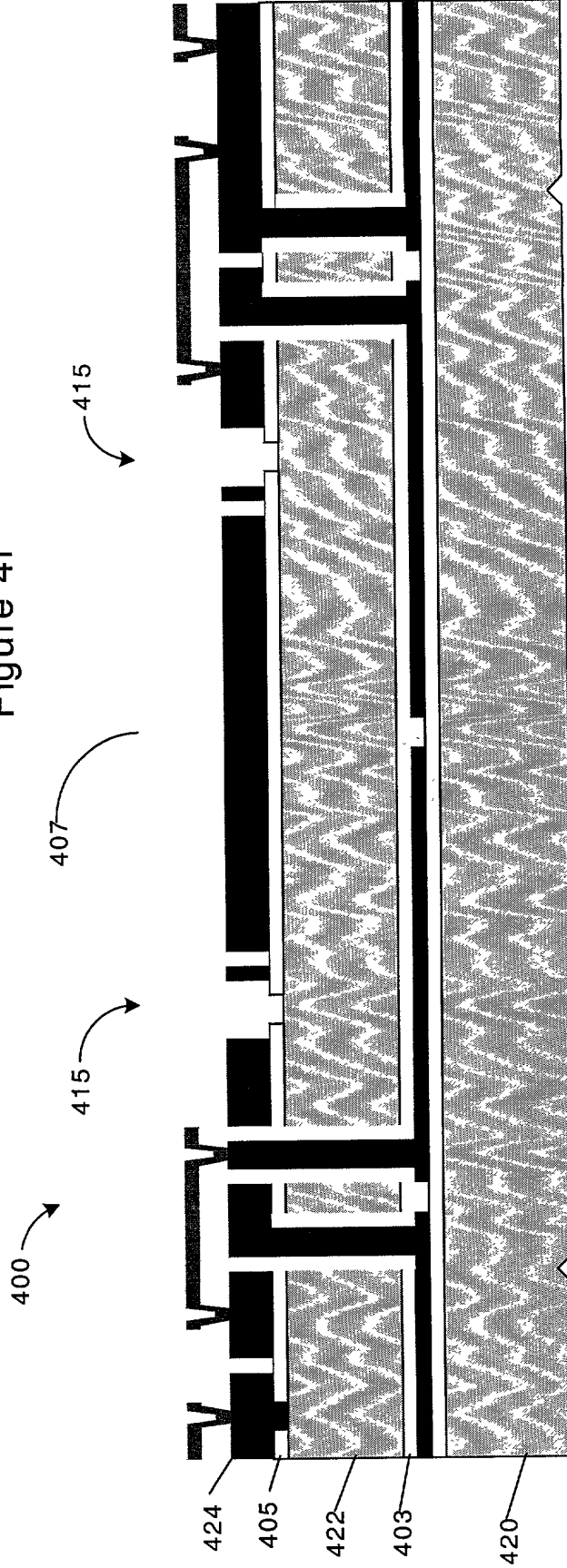


Figure 4j



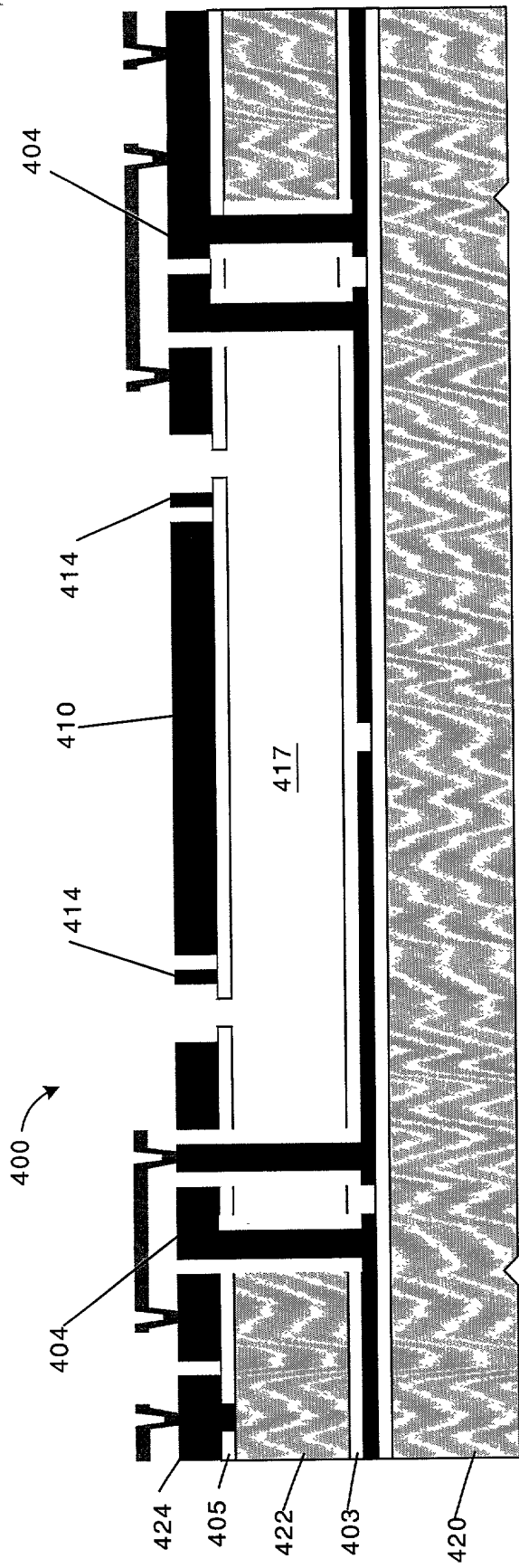


Figure 4k



Figure 4l

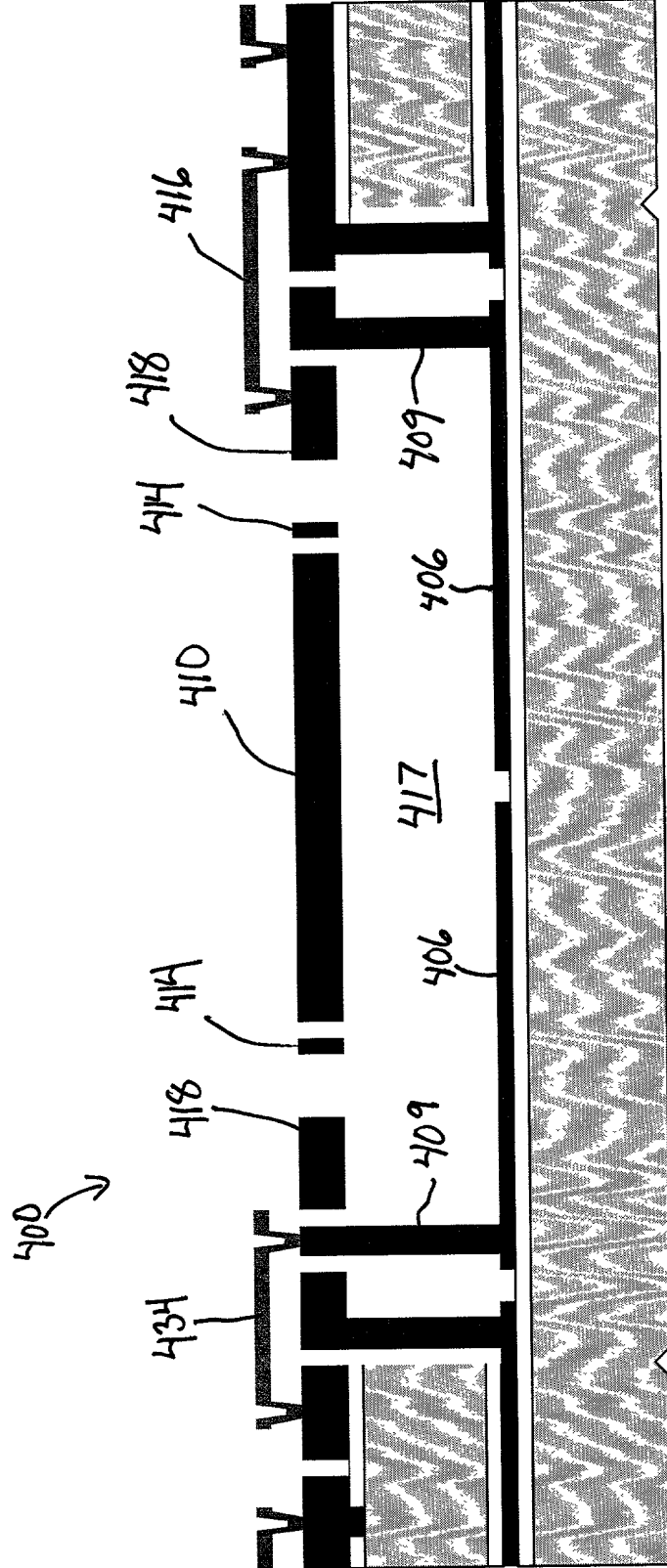


Figure 4m